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Vertical power MOSFET having thick metal layer to reduce distributed resistance and method of fabricating the same

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Abstract

The on-resistance of a vertical power transistor is substantially reduced by forming a thick metal layer on top of the relatively thin metal layer that is conventionally used to make contact with the individual transistor cells in the device. The thick metal layer is preferably plated electrolessly on the thin metal layer through an

opening that is formed in the passivation layer. 

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